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J105b U.S. PTO
09/941683



Date: August 30, 2001
Docket No.: 1152-0282P

Assistant Commissioner for Patents
Box PATENT APPLICATION
Washington, D.C. 20231

Sir:

Transmitted herewith for filing is the patent application of

Inventor(s): UCHIDA, Ren

For: TESTING METHOD AND TESTING DEVICE FOR SEMICONDUCTOR
INTEGRATED CIRCUITS

Enclosed are:

A specification consisting of 44 pages

9 sheet(s) of formal drawings

An assignment of the invention - \$40.00 Recording Fee

Certified copy of Priority Document(s)

Executed Declaration Original Photocopy

Applicant claims small entity status in accordance with 37 CFR 1.27

Application Data Sheet in accordance with 37 C.F.R. 1.76

Preliminary Amendment

Information Disclosure Statement, PTO-1449 and reference(s)

Other _____

Applicant requests early publication

The filing fee has been calculated as shown below:

		LARGE ENTITY		SMALL ENTITY		
FOR	NO. FILED	NO. EXTRA	RATE	Fee	RATE	Fee
BASIC FEE	***** ***** *****	***** ***** *****	***** ***** *****	\$710.00 or	***** ***** *****	\$355.00
TOTAL CLAIMS	8 - 20 =	0	x18 =	\$ 0.00 or	x 9 =	\$ 0.00
INDEPENDENT	2 - 3 =	0	x80 =	\$ 0.00 or	x 40 =	\$ 0.00
MULTIPLE DEPENDENT CLAIM PRESENTED	no		+270 =	\$ 0.00 or	+135 =	\$ 0.00
				TOTAL \$ 710.00	TOTAL \$ 0.00	

A check in the amount of \$ 750.00 to cover the filing fee and recording fee (if applicable) is enclosed.

Please charge Deposit Account No. 02-2448 in the amount of \$. A triplicate copy of this transmittal form is enclosed.

No fee is enclosed.

Please send correspondence to:

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If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. 1.16 or under 37 C.F.R. 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

By Charles B. Greider #20271

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